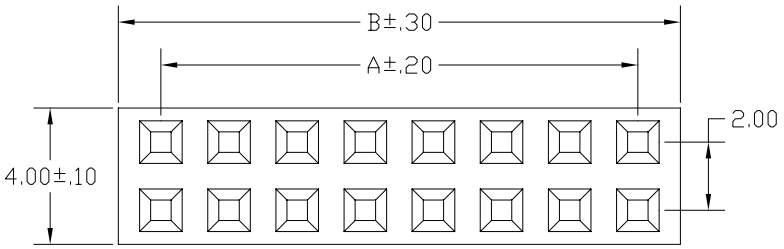
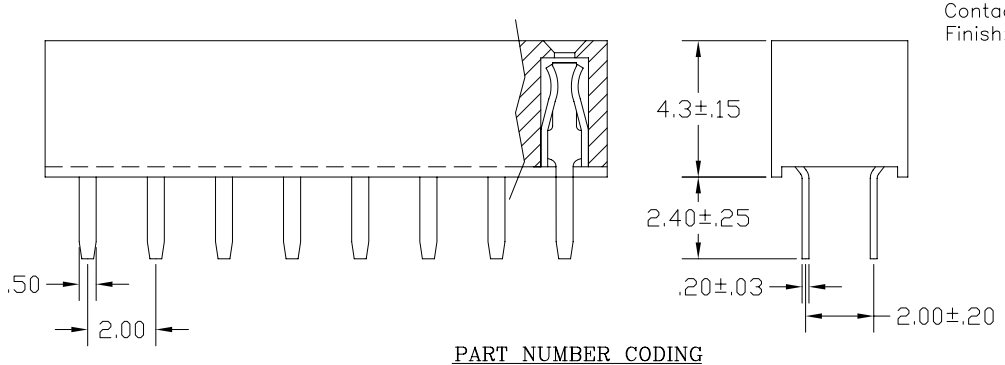
No. of Positions	DIMENSIONS A	B
3	4.00	6.50
4	6.00	8.50
5	8.00	10.50
6	10.00	12.50
7	12.00	14.50
8	14.00	16.50
9	16.00	18.50
10	18.00	20.50
11	20.00	22.50
12	22.00	24.50
13	24.00	26.50
14	26.00	28.50
15	28.00	30.50
16	30.00	32.50
17	32.00	34.50
18	34.00	36.50
19	36.00	38.50
20	38.00	40.50
21	40.00	42.50
22	42.00	44.50
23	44.00	46.50
24	46.00	48.50
25	48.00	50.50
26	50.00	52.50
27	52.00	54.50
28	54.00	56.50
29	56.00	58.50
30	58.00	60.50
31	60.00	62.50
32	62.00	64.50
33	64.00	66.50
34	66.00	68.50
35	68.00	70.50
36	70.00	72.50
37	72.00	74.50
38	74.00	76.50
39	76.00	78.50
40	78.00	80.50

NOTE: Number of contacts (pins) is not the same as the number of positions. For dual row connector, each position has 2 contacts.





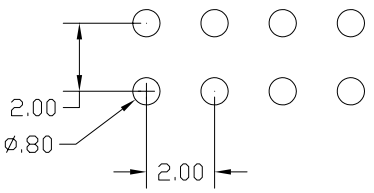
REV.	ECO	DESCRIPTION	DATE	BY
A	649	INITIAL RELEASE	6/16/2005	TT
B	724	B DIM .5mm LONGER INSULATOR FROM 4.0 TO 4.3mm	9/2/2005	TT
C	1126	UPDATE SPECIFICATIONS	9/12/2006	MV

SPECIFICATIONS

Current Rating:	1 Amp
Insulator resistance:	5000 Mega Ohms Min.
Contact resistance:	20 milli Ohm Max.
Dielectric withstanding:	500 VAC
Operating Temperature:	−40°C to +105°C
Contact Material:	Phosphor Bronze
Finish:	Gold flash overall

P.C. BOARD LAYOUT

RECOMMENDED



PART NUMBER CODING

xPPNxx2AFCN−RC

- xPPNxx2AFCN−RC
  - NUMBER OF POSITIONS (3 − 40)
  - P = Polyester, UL 94V−0 *Processing Temp. = 210°C for 5 Secs.
  - N = Nylon 6−T Processing Temp. = 260°C for 10 Secs.

*Indicated Temperature and time is for component Insulator. Higher processing temperaures may be used, provided heat is applied from back side of PCB, and Insulator does not exceed indicated temperature and time.

ROHS COMPLIANT

Sullins Electronics San Marcos, CA www.SullinsElectronics.com	GENERAL TOLERANCE: XX. ± / XXX. ±; X. ± .20 / .XXX ±; .X ± .10 / X.° ±; .XX ± .05 / .X° ±	SCALE NTS	DRAWN TT	DATE 6/16/2005	DWG. NO. 10497	TITLE 2.0mm FEMALE HEADER 4.3mm INSULATOR	REV. C
		UNIT MM	CHECK MNH	DATE 6/16/2005	PART NO. xPPNxx2AFCN−RC		SHEET
		SIZE	APPROVE	DATE			